

	L #	Hits	Search Text	Time Stamp
1	L1	13497	fifo	2000/03/29 09:28
2	L2	5326	(circular or ring or elastic) adj2 (buffer\$1 or memor\$3)	2000/03/29 10:11
3	L3	37052	(select\$4 or choos\$3) near8 (destination or target or receiver)	2000/03/29 10:17
4	L4	2025	(1 or 2) and 3	2000/03/29 10:18
5	L5	168	(3 same (2 OR 1))	2000/03/29 10:18
6	L6	69304	(sender or transmitter) same (receiver or target or destination) or transceiver	2000/03/29 10:22
7	L8	80	5 and 6	2000/03/29 10:24
8	L9	2659	3 with (register or bit)	2000/03/29 10:29
9	L10	56	(1 or 2) same 9	2000/03/29 10:29
10	L12	38	6 and 10	2000/03/29 10:35
11	L13	173033	register	2000/03/29 10:39
12	L14	4494	(1 or 2) with 13	2000/03/29 10:39
13	L15	12435	(destination or target or node) near4 (register\$1 or bit\$1)	2000/03/29 10:46
14	L16	349	(1 or 2) same 15	2000/03/29 10:46
15	L17	153	(15 with (2 OR 1))	2000/03/29 10:46
16	L18	1862	(gateway or operati\$2 adj2 mode) near4 (register\$1 or bit\$1)	2000/03/29 12:03
17	L19	130	(1 or 2) and 15 and 18	2000/03/29 12:03
18	L21	6	((18 same 15) AND (2 OR 1))	2000/03/29 12:05

	L #	Hits	Search Text	Time Stamp
19	L22	25	(1 or 2) same 18	2000/03/29 12:05
20	L23	0	17 and 22	2000/03/29 12:05
21	L24	49996	(transmitter or sender) near8 (receiver or destination or target)	2000/03/29 12:09
22	L25	66834	operati\$2 adj2 mode\$1	2000/03/29 12:13
23	L26	248	24 with 25	2000/03/29 12:13
24	L28	10	(1 or 2) and 26	2000/03/29 12:25
25	L29	3776	(1 or 2) same (packet or frame)	2000/03/29 12:39
26	L30	637	29 same (serial\$2)	2000/03/29 12:39

	Document ID	Issue Date	Title	Current OR
1	US 6014764 A	20000111	Providing test vectors with pattern chaining definition	714/738
2	US 5901301 A	19990504	Data processor and method of processing data	712/212
3	US 5870391 A	19990209	Wireless communication system using frequency hopping, and method of controlling the system	370/330
4	US 5867178 A	19990202	Computer system for displaying video and graphic data with reduced memory bandwidth	345/507
5	US 5768530 A	19980616	High speed integrated circuit interface for fibre channel communications	709/233
6	US 5754764 A	19980519	Combination of input output circuitry and local area network systems	709/200
7	US 5748875 A	19980505	Digital logic simulation/emulation system	714/29
8	US 5745731 A	19980428	Dual channel FIFO circuit with a single ported SRAM	711/149
9	US 5689438 A	19971118	Audio bit stream generator for high definition television utilizing personal computer, and control method therefor	700/94
10	US 5686965 A	19971111	Two-part synchronization scheme for digital video decoders	348/423
11	US 5617118 A	19970401	Mode dependent minimum FIFO fill level controls processor access to video memory	345/516
12	US 5606673 A	19970225	Small computer system interface (SCSI) controller	710/129

	Document ID	Issue Date	Title	Current OR
13	US 5543842 A	19960806	Apparatus and method for providing multiple video data streams from a single source	348/387
14	US 5539464 A	19960723	Apparatus, systems and methods for generating displays from dual source composite data streams	348/388
15	US 5539465 A	19960723	Apparatus, systems and methods for providing multiple video data streams from a single source	348/388
16	US 5515329 A	19960507	Variable-size first in first out memory with data manipulation capabilities	365/221
17	US 5455626 A	19951003	Apparatus, systems and methods for providing multiple video data streams	348/385
18	US 5452006 A	19950919	Two-part synchronization scheme for digital video decoders from a single source	348/845.3
19	US 5428650 A	19950627	Method and device for selecting information usable by a local unit connected to a digital transmission system	375/377
20	US 5375142 A	19941220	Method and device for detecting and checking the template of digital messages transmitted to a receiver device	375/224
21	US 5144644 A	19920901	Soft trellis decoding	375/341

	Document ID	Issue Date	Title	Current OR
22	US 5129079 A		Computer system having subinstruction surveillance capability	712/211

	Document ID	Issue Date	Title	Current OR
23	US 4870704 A		Multicomputer digital processing system	710/120
24	US 4792731 A	19881220	Multi-room controlled for individual light controls	315/316
25	US 4602362 A	19860722	Information transport system	370/366

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DWPI	l3 and l6	18	<u>L7</u>
DWPI	asynchronous or (different or vary\$3) near3 (rate or speed or clock)	37116	<u>L6</u>
DWPI	l3 and l4	4	<u>L5</u>
DWPI	operati\$2 adj2 mode	14505	<u>L4</u>
DWPI	(l1 or l2) with serial\$2	160	<u>L3</u>
DWPI	(elastic or ring or circular) adj2 (buffer or memory)	1420	<u>L2</u>
DWPI	fifo	2967	<u>L1</u>

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File: DWPI

Nov 18, 1999

DERWENT-ACC-NO: 1999-613182

DERWENT-WEEK: 200002

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TITLE: Multifunction UART for modulating/demodulating IrDA data

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMIC	Image
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☐ 2. Document ID: US 5917948 A

Entry 2 of 31

File: DWPI

Jun 29, 1999

DERWENT-ACC-NO: 1999-442797

DERWENT-WEEK: 199937

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TITLE: Digital image sequence transmission reducing apparatus for autophoty television system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMIC	Image
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☐ 3. Document ID: JP 11055231 A

Entry 3 of 31

File: DWPI

Feb 26, 1999

DERWENT-ACC-NO: 1999-221352

DERWENT-WEEK: 199919

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TITLE: Serial interface device for communication between personal computer and modem - sets FIFO asynchronous serial communication format of UART based on data in shift register

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KMIC	Image
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☐ 4. Document ID: JP 10271099 A

Entry 4 of 31

File: DWPI

Oct 9, 1998

DERWENT-ACC-NO: 1998-601224

DERWENT-WEEK: 199851

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TITLE: Data receiver of digital network - has parallel to serial converter into which parallel data are loaded depending on interframe synchronizatio n signal and out of which serial data are read depending on internal triggering clock



Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 5. Document ID: JP 2998666 B2, JP 10190766 A

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File: DWPI

Jan 11, 2000

DERWENT-ACC-NO: 1998-452915

DERWENT-WEEK: 200007

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TITLE: Serial digital data format conversion method - involves writing serial digital data in predetermined format in FIFO memory which is asynchronously controllable

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 6. Document ID: EP 885418 A1, WO 9733230 A1, AU 9721299 A

Entry 6 of 31

File: DWPI

Dec 23, 1998

DERWENT-ACC-NO: 1997-457714

DERWENT-WEEK: 199904

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TITLE: Asynchronous data pipe for automatic management of transfers between application and bus structure - has applications setting definition registers and data pipe that feeds data to/from FIFO's and high speed serial bus interface

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 7. Document ID: JP 09162828 A

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File: DWPI

Jun 20, 1997

DERWENT-ACC-NO: 1997-379108

DERWENT-WEEK: 199735

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TITLE: Video signal multiplex transmission apparatus - has isolation circuit that performs word synchronisation of main channel and sub channel video signals using timing reference signal, and separates video signals

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 8. Document ID: JP 09074400 A

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File: DWPI

Mar 18, 1997

DERWENT-ACC-NO: 1997-233757

DERWENT-WEEK: 199721

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TITLE: Compression=expansion buffer for data communication of e.g. satellite communication system, mobile system - uses switches to respectively turn ON operation of first=in=first=out memories to which write=in and reading of data are performed at different clock speeds, during data transmission and reception

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 9. Document ID: US 5515329 A

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File: DWPI

May 7, 1996

DERWENT-ACC-NO: 1996-239060  
DERWENT-WEEK: 199624  
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TITLE: Variable size FIFO memory system with large DRAM array to hold

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KVMC	Image
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☐ 10. Document ID: US 5502678 A

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File: DWPI

Mar 26, 1996

DERWENT-ACC-NO: 1996-179524  
DERWENT-WEEK: 199618  
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TITLE: Full memory chip long write testing method for memories, such as SRAM, multi-port and FIFO memories, to identify leakage problem - by dividing memory chip into memory blocks and subjecting each memory block to long write test mode and exiting into read disturb mode, as well as normal operating mode in serial fashion

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KVMC	Image
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☐ 11. Document ID: US 5491498 A

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File: DWPI

Feb 13, 1996

DERWENT-ACC-NO: 1996-116519  
DERWENT-WEEK: 199612  
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TITLE: Graphics/audio controller supporting delivery of digital audio signals in graphic controller - accommodates asynchronous or synchronous audio access with memory control unit receiving audio and video data request signals

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KVMC	Image
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☐ 12. Document ID: DE 4226952 A1

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File: DWPI

Feb 17, 1994

DERWENT-ACC-NO: 1994-058248  
DERWENT-WEEK: 199408  
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TITLE: Frame synchroniser for telemetry system - has pair of FIFO memory stages operating alternately and coupled to serial to parallel converter for high rates

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KVMC	Image
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☐ 13. Document ID: US 5287458 A

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File: DWPI

Feb 15, 1994

DERWENT-ACC-NO: 1994-057572  
DERWENT-WEEK: 199407  
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TITLE: Buffered asynchronous communications element - comprises transmitter FIFO receiving parallel data transfers from CPU and sending data to transmitter shift register

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KVMC	Image
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☐ 14. Document ID: WO 9402808 A1, DE 69306582 E, US 5406369 A, EP 650578 A1, JP

## 07509313 W, EP 650578 B1

Entry 14 of 31

File: DWPI

Feb 3, 1994

DERWENT-ACC-NO: 1994-048998

DERWENT-WEEK: 199709

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TITLE: Laser gyro microprocessor configuration and control device - uses laser gyro control device, connected to laser gyro, to send configuration and control commands to laser gyro

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	K/MC	Image
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☐ 15. Document ID: EP 574144 A2, US 5357236 A, EP 574144 A3

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File: DWPI

Dec 15, 1993

DERWENT-ACC-NO: 1993-396814

DERWENT-WEEK: 199913

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TITLE: Difference flag circuitry for FIFO memory - has parallel difference circuits to form comparison of read and write counters followed by boolean operations to decide difference flags

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	K/MC	Image
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☐ 16. Document ID: US 5241660 A

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File: DWPI

Aug 31, 1993

DERWENT-ACC-NO: 1993-287991

DERWENT-WEEK: 199336

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TITLE: Buffered asynchronous communications element with receive-transmit control and status reporting - has receiver shift register obtaining serial data transfers from communication station and transferring to CPU via FIFO, and transmitter receiving transfers from CPU via other FIFO

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	K/MC	Image
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☐ 17. Document ID: US 4839893 A

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File: DWPI

Jun 13, 1989

DERWENT-ACC-NO: 1989-220410

DERWENT-WEEK: 198930

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TITLE: Telecommunications first-in first-out circuitry - has sensor to detect if storage location being read from at time of write frame sync. signal is received meets set criterion

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	K/MC	Image
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☐ 18. Document ID: US 4823312 A

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File: DWPI

Apr 18, 1989

DERWENT-ACC-NO: 1989-138129

DERWENT-WEEK: 198918

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TITLE: Async. communication element with user-selectable FIFO(s) - serving as transmitter and receiver buffers to reduce CPU interrupt overhead

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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- ☐ 19. Document ID: EP 285334 A, DE 3885136 G, EP 285334 B1, JP 63258140 A, US 4949333 A

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File: DWPI

Oct 5, 1988

DERWENT-ACC-NO: 1988-280103

DERWENT-WEEK: 198840

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TITLE: Universal async. receiver-transmitter - has parity error and special character recogniser unit on receive side flagging characters placed on FIFO

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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- ☐ 20. Document ID: JP 63182926 A

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File: DWPI

Jul 28, 1988

DERWENT-ACC-NO: 1988-253138

DERWENT-WEEK: 198836

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TITLE: TDM line rate converter circuit reducing effect of bit error - provides data to serial-parallel converter connected to FIFO memories where data is read by different clock NoAbstract Dwg 1/7

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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- ☐ 21. Document ID: EP 274709 A, DE 3750657 G, EP 274709 B1, JP 63153941 A, US 4862461 A

Entry 21 of 31

File: DWPI

Jul 20, 1988

DERWENT-ACC-NO: 1988-199241

DERWENT-WEEK: 198829

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TITLE: Asynchronous digital network serial communication and data recovery - has processor which analyses all nodes at inception of error state with all signal information removed

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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- ☐ 22. Document ID: EP 454526 A, CA 2040957 A, FR 2661526 A

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File: DWPI

Oct 30, 1991

DERWENT-ACC-NO: 1991-319584

DERWENT-WEEK: 199144

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TITLE: Symmetric multiprocessor computer system - includes data links and dedicated inter-processor channels allowing high transaction rate

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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- ☐ 23. Document ID: CA 2025511 A, US 5218683 A

Entry 23 of 31

File: DWPI

May 1, 1991

DERWENT-ACC-NO: 1991-201016

DERWENT-WEEK: 199128

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TITLE: Status word modification device for data communication device - has two uarts contg. fifo buffers, status buffers and control registers allowing status words to pass through interface

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 24. Document ID: EP 419067 A

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File: DWPI

Mar 27, 1991

DERWENT-ACC-NO: 1991-088680

DERWENT-WEEK: 199113

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TITLE: Computer system with apparatus for conducting serial port - reduces number of system interrupts by incorporating uart hardware with FIFO capabilities and buffering control

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 25. Document ID: JP 03018143 A

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File: DWPI

Jan 25, 1991

DERWENT-ACC-NO: 1991-069530

DERWENT-WEEK: 199110

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TITLE: Elastic buffer for asynchronous digital communication system - has memories to which parallel data converted from incoming serial data are input NoAbstract  
Dwg 1/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 26. Document ID: US 4965794 A

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File: DWPI

Oct 23, 1990

DERWENT-ACC-NO: 1990-341953

DERWENT-WEEK: 199045

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TITLE: Telecommunications FIFO - has memory for receiving and storing synchronous serial bit stream of N-bit packets on data and for reading out corresp. stored data

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 27. Document ID: DE 68925322 E, EP 359137 A, US 5140679 A, US 5199105 A, US 5228130 A, EP 359137 B1

Entry 27 of 31

File: DWPI

Feb 15, 1996

DERWENT-ACC-NO: 1990-084971

DERWENT-WEEK: 199612

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TITLE: Universal async. receiver-transmitter for data communications - has counter connected to receiver to determine number of valid data characters received before exception

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 28. Document ID: BE 903857 A, AU 8551044 A, BR 8506028 A, CN 8507302 A, DE 3586988 G, DK 8505780 A, EP 185936 A, EP 185936 B1, ES 8706357 A, HU 41933 T, JP 61144146 A, PT 81642 A, US 4639910 A, ZA 8508753 A

Entry 28 of 31

File: DWPI

Jun 16, 1986

DERWENT-ACC-NO: 1986-169633

DERWENT-WEEK: 198627

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TITLE: Telecommunications switching system and priority device - makes communication paths depending on type of information transfer

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 29. Document ID: US 4586189 A, ES 8606758 A

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File: DWPI

Apr 29, 1986

DERWENT-ACC-NO: 1986-131314

DERWENT-WEEK: 198620

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TITLE: Asynchronous to synchronous data interface - has UART performing band rate conversion and clock alignment with FIFO memory storing data during re-alignment

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 30. Document ID: US 4187394 A

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Feb 5, 1980

DERWENT-ACC-NO: 1980-B6039C

DERWENT-WEEK: 198007

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TITLE: Duplex data link for noisy environments - has separated and delayed data clock pulses to synchronise serial-to-parallel converters

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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☐ 31. Document ID: US 3936600 A

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Feb 3, 1976

DERWENT-ACC-NO: 1976-B5962X

DERWENT-WEEK: 197607

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TITLE: Keyboard-printer terminal interface for data processing - communicates in serial code and has buffer store allowing asynchronous operation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image
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